

"Reply under 37 CFR 1.116
--Expedited Procedure-Examining Group 2183

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Hung T. Nguyen, et al.

Corres, and Mail

Serial No.:

10/007,498

BOX AF

Filed:

November 13, 2001

Title:

PIPELINED MULTIPLY-ACCUMULATE UNIT AND OUT-OF-ORDER

COMPLETION LOGIC FOR A SUPERSCALAR DIGITAL SIGNAL

PROCESSOR AND METHOD OF OPERATION THEREOF

Grp./A.U.:

2183

Examiner:

Meonske, Tonia L.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Thereby certify that this correspondence is being deposited with the United States Postal
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Sir:

## REMARKS UNDER 37 C.F.R. § 1.116

In response to the Examiner's Final Action mailed January 13, 2005, the Applicants present the following arguments: